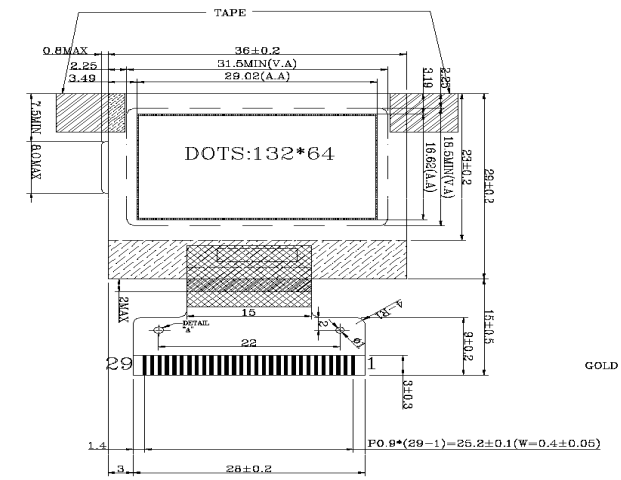
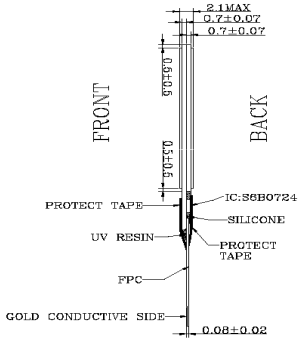
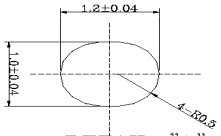
SAZENTO SAG13264CG Graphic: 132 x 64 dots

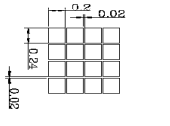




PIN DESCRIPTION

NO	SYMBOL	NO	SYMBOL
1	C68	17	D6
2	V0	18	D5
3	V4	19	D4
4	V3	20	D3
5	V2	21	D2
6	V1	22	D1
7	C2-	23	D0
8	C2+	24	E_RD
9	C1+	25	RW_WR
10	C1-	26	RS
11	C3+	27	REEST
12	C4+	28	CS1B
13	VOUT	29	NC
14	VSS		
15	VDD		
16	D7		





NOTES:
1. DISPLAY TYPE:FSTN
2. VIEWING DIRECTION:6 O'CLOCK
3. POLARIZER TYPE:TRANSFLECTIVE/POSITIVE
4. DRIVE METHOD:1/65DUTY,1/9BIAS
5. LCD DRIVING VOLTAGE:9V
6. LOGIC SUPPLY VOLTAGE:3.0V
7. OPERATING TEMP:-20°C TO 70°C
8. STORAGE TEMP:-30°C TO 80°C
9. CONTROLLER IC:S6B0724A01-B0CY
10. CONNECTOR TYPE:FPC

FEATURE

1. FSTN, Positive, Transflective

2. IC: Samsung S6B0724A01-B0CY

3. 1/65Duty, 1/9Bias, 6 O'clock

4. Backlight: N/A, Display dot: Black, Background: White

Pin NO	Symbol	Function
1	C68	Microprocessor interface select input pin in parallel mode
2	V0	LCD driver supply voltages
3	V4	
4	V3	
5	V2	
6	V1	
7	C2-	Capacitor 2 negative connection pin for voltage onverter
8	C2+	Capacitor 2 positive connection pin for voltage converter
9	C1+	Capacitor 1 positive connection pin for voltage converter
10	C1-	Capacitor 1 negative connection pin for voltage converter
11	C3+	Capacitor 3 positive connection pin for voltage converter
12	C4+	Capacitor 4 positive connection pin for voltage converter
13	VOUT	Voltage converter input/output pin
14	VSS	Ground
15	VDD	Power supply
16-23	D7-D0	8-bit-bi-directional data bus that is connected to the standard
24	E_RD	Read enable clock input pin
25	RW_WR	Read / Write execution control pin
26	RS	Register select input pin.
27	REST	Reset input pin
28	CS1B	Chip select input pins Data / instruction I/O is enabled only when CS1B is "L"
29	NC	No connection

MECHANICAL DATA		
Item	**Standard Value**	**Unit**
Module Dimension	36.0(W) × 44.0(H) × 2.1MAX(T)	mm
Viewing Area	31.5MIN(W) × 18.5MIN(H)	mm
Dot Size	0.20(W) × 0.24(H)	mm
Dot Pitch	0.22(W) × 0.26(H)	mm

ABSOLUTE MAXIMUM RATING					
Item	**Symbol**	**Standard Value**			**Unit**
		min	**typ**	**max**	
Supply Voltage For Logic	VDD-VSS	-0.3	–	+7.0	V
Input Voltage	VIN	-0.3	–	VDD+0.3	V

ELECTRONICAL CHARACTERISTICS						
Item	**Symbol**	**Condition**	**Standard Value**			**Unit**
			min	**typ**	**max**	
Input Voltage	VIH	VDD=3.0V±5%	0.8VDD	–	VDD	V
	VIL		VSS	–	0.2VDD	
Current Consumption	IDD	VDD=3.0V±5% V0-VSS=9.0V Fflm=75Hz	–	0.22	1.0	mA
Logic Supply Voltage	VDD-VSS	Ta=0~50℃	2.4	3.0	3.6	V
Supply Voltage For LCD	V0-VSS	Ta-25℃	8.7	9.0	9.3	V
Frame Frequency	fFLM	VDD=3.0V±5%	65	75	85	Hz