



**FEATURE**

1. STN, Positive, Transflective
2. IC: Solomon SSD1815T2
3. 1/65Duty, 1/9Bias, 6 O'clock
4. Display dot: Blue, Background: Gray

Pin NO	Symbol	Function
1	Dummy	No connected
2	CL	Display clock input/output. When SSD1815 is used in master/slave mode (multi-chip). The CL pins must be connected to each other.
3	/CS1	Chip select inputs pins.
4	/RES	Reset input pin. When RESETB is "L", initialization is executed.
5	D/C	Register select input
6	R/W	When interfacing to a 6800 series MPU: Active High
7	E/RD	When interfacing to a 6800series MPU: Active High
8~15	D0~D7	When the serial interface selected(PS="L")
16	VDD	Power supply
17	VSS	Ground
18	VEE	Power supply
19	C3N	When internal DC-DC voltage converter is used, external capacitor is connected between these pin. Different connection will result in different DC-DC converter multiple factor, 2X, 3X or 4X.
20	C1P	
21	C1N	
22	C2N	
23	C2P	
24	VFS	This pin provide an external voltage reference for the internal voltage regulator.

MECHANICAL DATA		
Item	Standard Value	Unit
Module Dimension	85(W) × 64.7(H) × 2.9MAX(T)	mm
Viewing Area	80(W) × 40(H)	mm
Dot Size	0.45(W) × 0.45(H)	mm
Dot Pitch	0.485(W) × 0.485(H)	mm

ABSOLUTE MAXIMUM RATING					
Item	Symbol	Standard Value			Unit
		min	typ	max	
Supply Voltage For Logic	VDD-VSS	-0.3	-	+4.0	V
Input Voltage	VIN	VSS-0.3	-	VDD+0.3	V

ELECTRONICAL CHARACTERISTICS						
Item	Symbol	Condition	Standard Value			Unit
			min	typ	max	
Input Voltage	VIH		0.8VDD	-	VDD	V
	VIL		VSS	-	0.2VDD	
Output Voltage	VOH	IOH=-0.1mA	0.9VDD	-	VDD	V
	VOL	IOL=0.1mA	0	-	0.1VDD	
Current Consumption	IDD	VDD-VSS=3.0V	-	0.30	2.0	mA
Supply Voltage For Logic	VDD-VSS	-	2.4	3.0	3.5	V
Supply Voltage For LCD	Vop=VD D-VL6	Ta=25°C	7.9	8.3	8.9	V