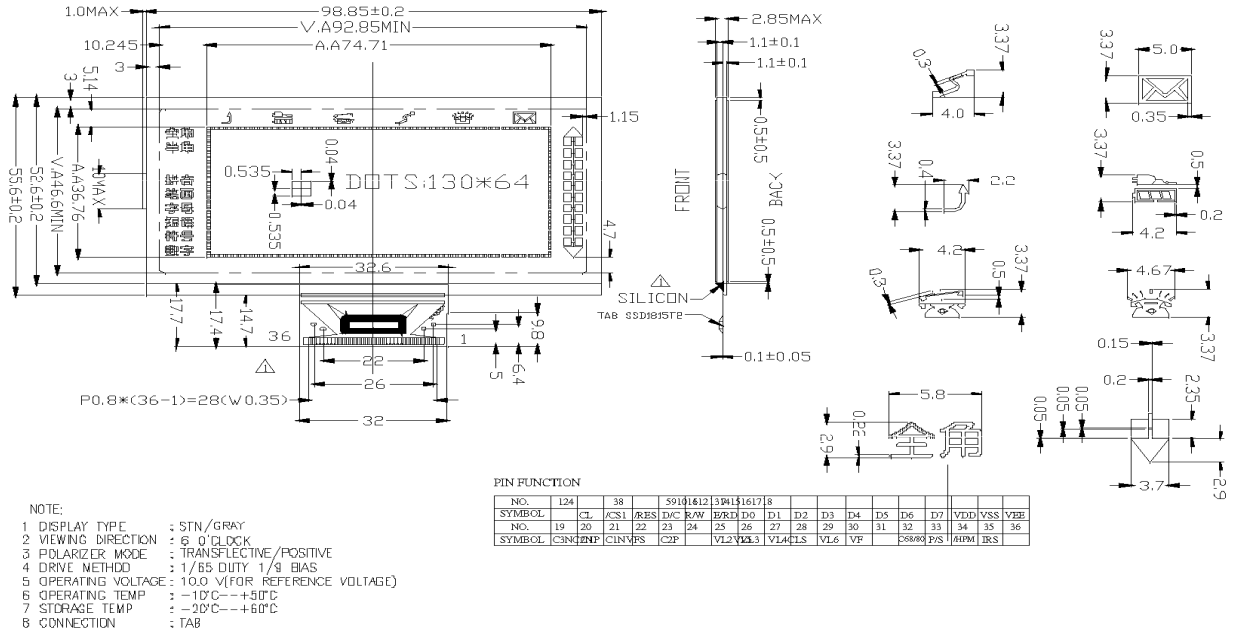
SAZENTO SAG13064BG — Graphic 130 x 64 dots



FEATURE

1. STN, Positive, Transflective

2. IC: Solomon SSD1815T2

3. 1/65Duty, 1/9Bias, 6 O'clock

4. Display dot: Blue, Background: Gray

Pin NO	Symbol	Function
1	Dummy	No connected
2	CL	Display clock input/output.
3	/CS1	Chip select inputs pins.
4	/RES	Reset input pin.
5	D/C	Register select input
6	R/W	When interfacing to a 6800 series MPU: Active High
7	E/RD	When interfacing to a 6800 series MPU: active high
8-15	D0-D7	When the serial interface selected(PS=" L") D7: serial input data (SDA) D6: serial input clock (SCK) D5 to D0: high impedance.
16	VDD	Power supply
17	VSS	Ground
18	VEE	Power supply
19	C3N	When internal DC-DC voltage converter is used, external capacitor is connected between these pin. Different connection will result in different DC-DC converter multiple factor, 2X, 3X or 4X.
20	C1P	
21	C1N	
22	C2N	
23	C2P	
24	VFS	This pin provide an external voltage reference for the internal voltage regulator.

MECHANICAL DATA		
Item	**Standard Value**	**Unit**
Module Dimension	95.85(W) × 70.3(H) × 2.85MAX(T)	mm
Viewing Area	92.85(W) × 46.6(H)	mm
Dot Size	0.535(W) × 0.535(H)	mm
Dot Pitch	0.575(W) × 0.575(H)	mm

ABSOLUTE MAXIMUM RATING					
Item	**Symbol**	**Standard Value**			**Unit**
		min	**typ**	**max**	
Supply Voltage For Logic	VDD-VSS	-0.3	–	+4.0	V
Input Voltage	VIN	VSS-0.3	–	VDD+0.3	V

ELECTRONICAL CHARACTERISTICS						
Item	**Symbol**	**Condition**	**Standard Value**			**Unit**
			min	**typ**	**max**	
Input Voltage	VIH		0.8VDD	–	VDD	V
	VIL		VSS	–	0.2VDD	
Output Voltage	VOH	IOH=-0.1mA	0.9VDD	–	VDD	V
	VOL	IOL=0.1mA	0	–	0.1VDD	
Current Consumption	IDD	VDD-VSS =3.0V	–	0.34	1.0	mA
Supply Voltage For Logic	VDD-VSS	–	2.4	3.0	3.5	V
Supply Voltage For LCD	Vop=VDD -VL6	Ta=25℃	8.0	8.6	9.5	V